

ABSTRACT OF THE DISCLOSURE

A device such as a liquid crystal display is provide, in which every pixel can sufficiently realize writing of a video signal into a storage capacitor. The liquid crystal display device of the present invention includes left and right gate drivers. The left gate driver is connected to supply selection signals to TFTs of pixels of a left half of a pixel portion. The right gate driver is connected to supply selection signals to TFTs of pixels of a right half of the pixel portion. In the liquid crystal display device of the present invention, timing when the left gate driver outputs a selection signal to a gate signal line connected to a pixel of a column is different from timing when the right gate driver outputs a selection signal to a gate signal line connected to a pixel of the same row as the pixel.

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